Job Description for Publication:

Position:	Junior Verification Engineer
Job Description:	The Verification Engineer is required to work closely with Ceragon VLSI group in designing and developing chips, participating in emerging talented group. The job offers the opportunity to verify complex devices from initial stages of development and design over ASIC or FPGA platforms. It also offers exposure to definition of silicon making methodologies from architecture towards release to production, working closely with a dynamic and motivated team. Verification Engineer responsibilities: ● Develop verification Environment for various platforms – FPGA or ASIC. Implementing under top notch methodologies. ● Responsible for Various IPs (PHY, Networking IPs etc) and SOC Verification ● Defining Test plans, coverage plans etc ● Working closely with Ceragon system team ensuring correct implementation of the Ceragon FibAir products requirements
Job Requirements:	The ideal candidate has the following: Must: ● Bachelor degree in Automatics/Computers, Electrical Engineering ● Familiar with digital circuits/hardware design (Verilog or VHDL) ● Familiar with object oriented programming (e.g. Java, C++) ● Familiar with Linux and shell scripting ● At least Intermediate level in English (both speaking and writing) Advantage: ● Previous experience or involvement in personal software/hardware projects ● Familiar with digital circuits simulators (e.g.: ModelSim) ● Familiar with RISC processors ● Familiarity with Specman or System Verilog based verification ● Background in Networking IPs and SOC architecture We are looking for candidates capable of self-learning and working in a dynamic environment, with excellent communication skills and team player, capable of grasping an overall view of complex systems

Other information	For more clarifications contact florentinaa@ceragon.com
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