

Job Description for Publication:

Position:	Verification Engineer
Job Description:	<p>The Verification Engineer is required to work closely with Ceragon VLSI group in designing and developing chips, participating in emerging talented group. The job offers the opportunity to verify complex devices from initial stages of development and design over AISC or FPGA platforms. It also offers exposure to definition of silicon making methodologies from architecture towards release to production, working closely with a dynamic and motivated team.</p> <p>Verification Engineer responsibilities:</p> <ul style="list-style-type: none">● Develop verification Environment for various platforms – FPGA or ASIC. Implementing under top notch methodologies.● Responsible for Various IPs (PHY, Networking IPs etc) and SOC Verification● Defining Test plans, coverage plans etc● Working closely with Ceragon system team ensuring correct implementation wrt the Ceragon FibAir products requirements
Job Requirements:	<p>The ideal candidate has the following:</p> <p>Must:</p> <ul style="list-style-type: none">● Preferred university background – Automatics/Computers, Electrical Engineering● At least 3 years experience with Specman or System Verilog based verification (specman is preferred)● In depth understanding of overall verification methodologies● Background in Networking IPs and SOC architecture● Understanding of verification and design practices <p>Advantage:</p> <ul style="list-style-type: none">● Experience with RISC processors● Experience with C coding● Verilog <p>We are looking for candidates capable of self-learning and working in a dynamic environment, with excellent communication skills and team player, capable of grasping an overall view of complex systems.</p>
Other information	For more clarifications contact florentinaa@ceragon.com

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